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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/733,799		12/12/2003	Takashi Noma	492322015200	8803	
25227	7590	11/08/2005		EXAM	EXAMINER	
1.101-0-0		ERSTER LLP	VU, D	VU, DAVID		
1650 TYSONS BOULEVARD SUITE 300				ART UNIT	PAPER NUMBER	
MCLEAN,		02		2818		
				DATE MAILED: 11/08/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		10/733,799	NOMA, TAKASHI				
•	Office Action Summary	Examiner	Art Unit				
		DAVID VU	2818				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. Properties of the period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time y within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status							
1)	Responsive to communication(s) filed on <u>30 August 2005</u> .						
2a)⊠							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	ion of Claims						
5)⊠	<u>'</u>						
Applicati	on Papers						
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on 12 December 2003 is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 							
Priority u	ınder 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
Attachmen	Ne)		·				
1) Notic	e of References Cited (PTO-892)	4) 🔲 Interview Summary	(PTO-413)				
2) 🔃 Notic 3) 🔀 Inforr	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date <u>10/13/05</u> .	Paper No(s)/Mail Da					

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1. Claims 1-4 are rejected under 35 U. S. C. 102(b) as being anticipated by Chroneos, Jr. et al. (US 6,805,279, herein after Chroneos, Jr.).

Chroneos, Jr., in related text and figures 5&6 discloses semiconductor device manufacturing method, comprising: providing a semiconductor wafer /circuit board 102 (col. 2, lines 43-46) provided with a plurality of metal portions (solder balls) on a surface of the semiconductor wafer; suspending the semiconductor wafer 102 on a plurality of pin 116 in a reflow furnace so that the metal portions are positioned upwardly in the reflow furnace (fig. 3); reflowing the metal portions on the surface of the semiconductor wafer suspended in the reflow furnace so that the metal portions form conductive terminals using a first heater disposed in the reflow furnace and facing the surface of the semiconductor wafer, wherein the semiconductor wafer is suspended by a plurality of pins (col. 5, lines 6-53).

Allowable Subject Matter

2. Claims 5-10 are allowed.

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3. The following is an examiner's statement of reason for allowance: the prior art of record, either singularly or in combination, does not disclose or suggest that semiconductor device manufacturing method, comprising: supporting the semiconductor wafer using a plurality of pins that are in contact with the first supporting substrate; and reflowing the metal portions so as to from conductive terminals using a first heater disposed to face the back surface of the semiconductor wafer while the semiconductor wafer is supported by the pins, as instantly claimed and in combination with the additionally claimed method steps.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Response to Arguments

4. Applicant's arguments with respect to claims 1-4 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the 6. examiner should be directed to David Vu whose telephone number is (571) 272-1798. The examiner can normally be reached on Monday-Friday from 8:00am to 5:00pm. If attempt to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR, Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

David Vu

November 06, 2005.